



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Boon et al.

Serial No.: 10/717,421

Filed: November 19, 2003

For: CASTELLATED CHIP-SCALE
PACKAGES AND METHODS FOR
FABRICATING THE SAME

Confirmation No.: 7753

Examiner: J. Mitchell

Group Art Unit: 2813

Attorney Docket No.: 2269-5660US
(02-1391.00/US)

CERTIFICATE OF MAILING

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
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Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 or PTO/SB/08 be considered by the Examiner and made of record. Copies of U.S. patents and U.S. patent publications are not being submitted pursuant to M.P.E.P. 609 III A(2). Copies of foreign patent documents and non-patent literature are enclosed pursuant to 37 C.F.R. § 1.98(a)(2).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Publication Date</u>	<u>Patentee</u>
US - 4,956,694	09/1990	Eide
US - 5,138,115	08/11/1992	Lam
US - 5,313,096	05/17/1994	Eide
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US - 6,004,867	12/21/1999	Kim et al.
US - 2002/0017398 A1	02/14/2002	Hacke et al.
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US - 2001/0009300 A1	07/26/2001	Sugimura
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US - 2003/0102160 A1	06/05/2003	Gaudiello et al.
US - 2003/0162326 A1	08/28/2003	Tsubosaki et al.
US - 6,656,827 B1	12/02/2003	Tsao et al.
US - 2004/0156177 A1	08/12/2004	Higashitani
US - 6,849,802 B2	02/01/2005	Song et al.
US - 6,855,572	02/15/2005	Jeung et al.
US - 6,856,023 B2	02/15/2005	Muta et al.

Foreign Patent Documents

<u>Document No.</u>	<u>Publication Date</u>	<u>Patentee</u>
JP 59-27549	02/1984	Morikuri et al.
JP 59-43556	03/1984	Sawairi
JP 5-75014	03/1993	Fujitsu LTD
JP 6-97225	04/1994	Yamaji
JP 2001-44361	02/2001	Sakagucki et al.

Other Documents

NN81055595: "Edge Mounted MLC Packaging Scheme. May 1981; IBM Technical Disclosure Bulletin, May 1981 US: Volume 23, Issue #12, Page #5595-5598; Publication Date: May 1, 1981.

Australian Search Report, 04/15/2005, 5 pages.

Applicants offer to supply any explanation or discussion of the documents which the Examiner feels is necessary or desirable and which is requested.

Serial No. 10/717,421

This Supplemental Information Disclosure Statement is filed before the mailing date of a first Office Action on the merits.

Respectfully submitted,



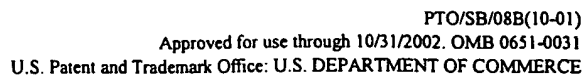
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BGP/dlm:eg

Enclosures: Form PTO-1449 or PTO/SB/08
Copy of non-US documents cited

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Substitute for form 1449A/PTO

(use as many sheets as necessary)

Sheet 2 of 2

Complete if Known

Application Number	10/717,421
Filing Date	November 19, 2003
First Named Inventor	Boon et al.
Group Art Unit	2813
Examiner Name	J. Mitchell
Attorney Docket Number	2269-56601US (02-1391 00/US)

[illegible]

**Examiner
Signature**

Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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